



ELT WFRTC

Software patterns and library solutions for low latency multithreading

Calle Rosenquist, ESO





Real time core

- CPU-based
- Single server (currently 128 core AMD EPYC Zen 3)
- Minimal Linux
- Control software
 - Single process
 - Statically linked
 - C++
 - Real-time threads running on isolated cores without scheduler ("isolcpus")

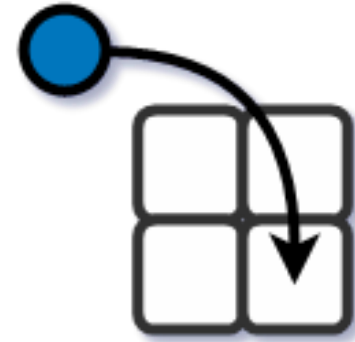


NUMA

non-uniform memory access

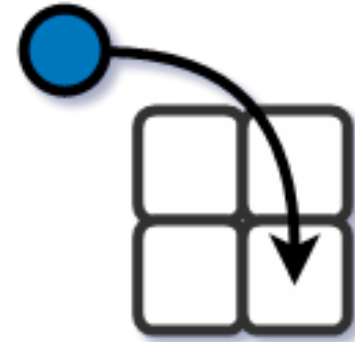
"Pinning" real time threads

- In Linux each thread have two masks
 - "CPUs allowed" - where to run
 - "Mems allowed" - where to allocate memory
- By *pinning* we allow only one core and its local memory



Memory allocations

- Critical heap memory allocated from specific NUMA nodes
- Avoid allocators that rely on thread policy
 - E.g. default operator `new`, `malloc()`
 - Not easily predictable
 - Memory may come from allocator "free list"





Support library: NUMA++

Creating pinned threads

```
1 #include <numapp/memory.hpp>
2 #include <numapp/thread.hpp>
3
4 template <class F, class... Args>
5 auto MakePinnedThread(int cpu, std::string_view name, F&& f, Args... args)
6     -> std::thread {
7     using namespace numapp;
8     auto node = GetNodeOfCpu(cpu);
9     if (!node.has_value()) {
10         throw std::invalid_argument("cpu invalid");
11     }
12
13     NumaPolicies policies;
14     policies.SetCpuAffinity(CpuAffinity::MakeBindCpu(cpu));
15     policies.SetMemPolicy(MemPolicy::MakeBindNode(*node));
16     return MakeThread(
17         name, policies, std::forward<F>(f), std::forward<Args>(args)...);
18 }
```

In short

- Lookup node from CPU core
- Create policies
 - Pin thread to core
 - Memory from local node
- Create thread with provided policies



Support library: NUMA++

Allocating memory from a specific node

```
1 #include <numapp/memory.hpp>
2 #include <numapp/mempolicy.hpp>
3
4 void* BindAlloc(std::size_t size, int node, std::error_code& ec) {
5     using namespace numapp;
6
7     MemPolicy policy = MemPolicy::MakeBindNode(node);
8     void* ptr = Allocate(size, policy, MemPolicyFlag::Strict, ec);
9     if (ec) {
10         return nullptr;
11     }
12     ec = MemLock(ptr, size, LockFlag::PreFault);
13     if (ec) {
14         return nullptr;
15     }
16     return ptr;
17 }
```

- Create policy to use single node
- Allocate
 - Uses `mmap()`
- Lock and pre-fault to trigger physical memory allocation



Inter-thread queues

Inter-thread queues

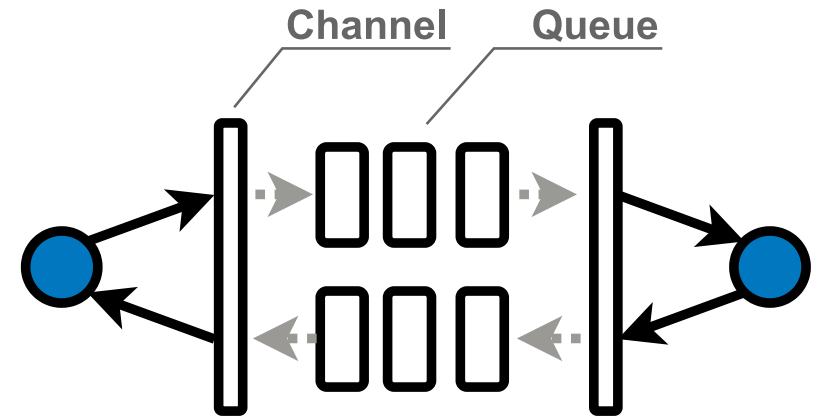
- Deliver data from single producer to single consumer without locking or blocking
- Fix capacity at construction to avoid reallocation
- Use *views* (e.g., pointer + size) to avoid unnecessary memory copies



Channels

WFRTC composition of two directional queues

- Second "free-list" queue
- Two queues forms a *Channel*
- `boost::lockfree::spsc_queue`



```

1  template <class Queue>
2  class Channel {
3  public:
4      using ValueType = typename Queue::value_type;
5      using QueueType = Queue;
6
7      explicit constexpr Channel(QueueType& read, QueueType& write) noexcept;
8
9      void Push(ValueType const& value);
10     bool TryPush(ValueType const& value) noexcept;
11
12     void Pop(ValueType& value);
13     bool TryPop(ValueType& value) noexcept;
14 };

```



Benchmark

- Measures push/pop operations under maximum contention
- Unpinned threads on unisolated cores

```
Running wfhrtcSpscQueueBenchmark
Run on (128 X 2450 MHz CPU s)
CPU Caches:
  L1 Data 32 KiB (x128)
  L1 Instruction 32 KiB (x128)
  L2 Unified 512 KiB (x128)
  L3 Unified 32768 KiB (x16)
Load Average: 0.15, 0.03, 0.11
-----
Benchmark                Time                CPU    Iterations  UserCounters...
-----
PopFixture/SpscPush      10.9 ns             10.9 ns    64391865  FullPercent=5.03837
PushFixture/SpscPop      34.9 ns             34.8 ns    19543691  EmptyPercent=2.70725
```

AMD EPYC 7763 (128 Core Zen 3 Milan)



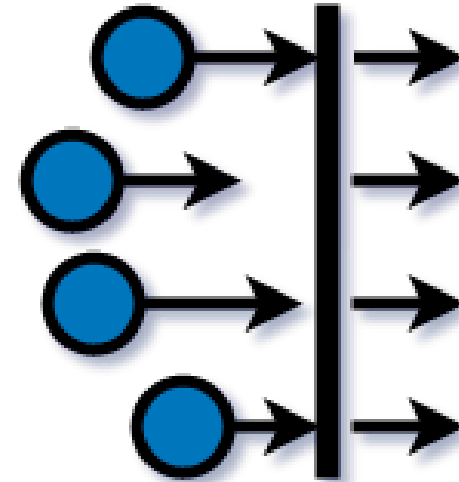
Scatter-gather

Scatter-gather

WFRTC use this to

- Offload work to multiple threads
- Gather results when all threads have completed

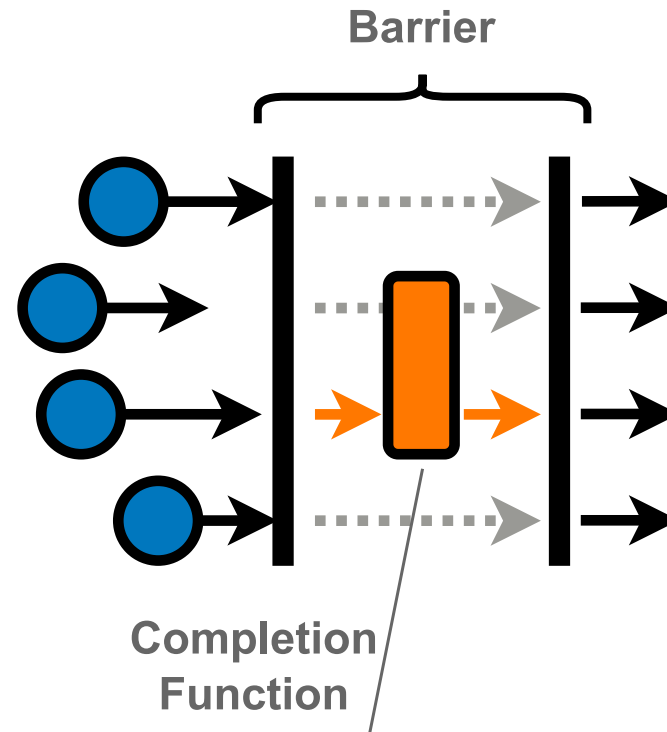
Achieved using the *barrier* primitive



Barrier

Introducing the barrier

- Threads *arrive and wait*
- When last thread arrives a *Completion Function* is invoked by one of the threads
 - *Completion Function* can **safely modify shared state**
- When *Completion Function* returns barrier is lifted and threads continue
- C++20 `std::barrier`



Barriers in WFRTC

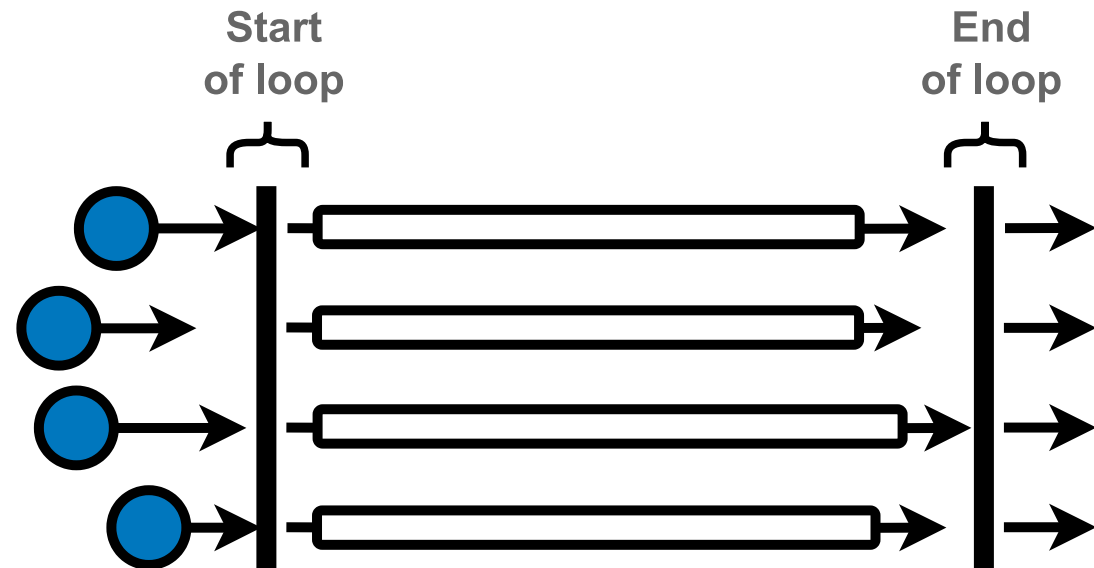
Computation loop

Start of loop barrier

- Wait for start condition, e.g.
 - Inputs from *channel*
 - State change *signal*
- Distribute data or signal to threads

End of loop barrier

- Gather results
- Push result to output *channel*





Support library: ion

ion::Barrier

- Semantics like `std::barrier`
- Busy-waits

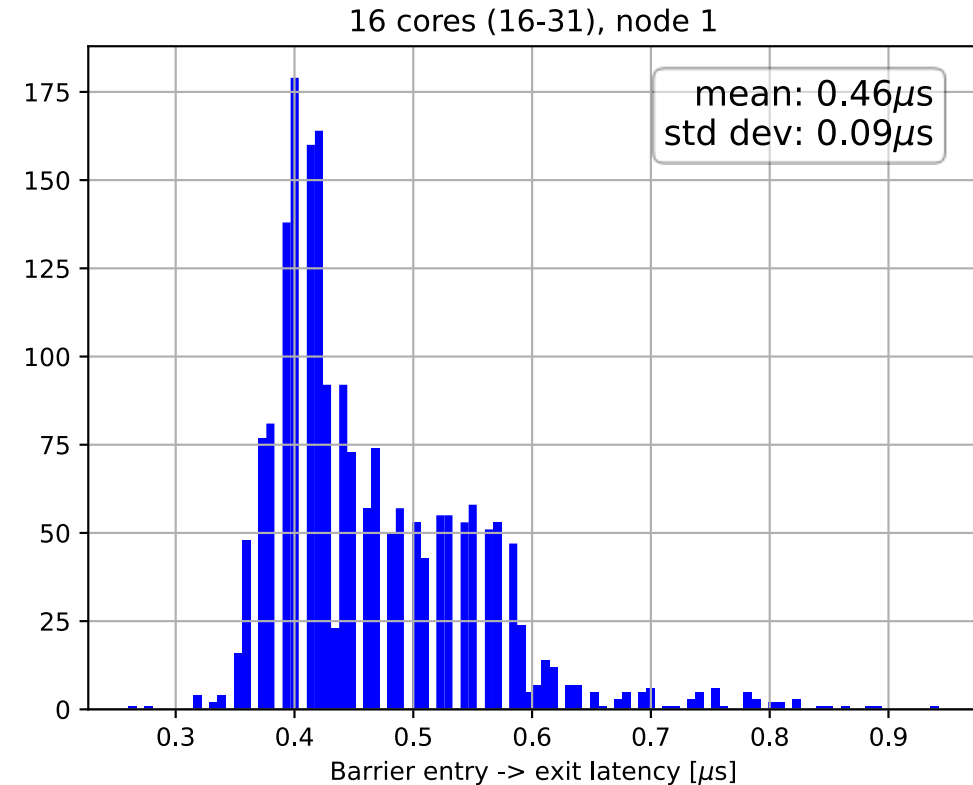
```
1  template <class CompletionFunction = *unspecified*>
2  class Barrier {
3  public:
4      Barrier(std::ptrdiff_t expected, CompletionFunction func);
5
6      void ArriveAndWait();
7
8      auto Arrive(std::ptrdiff_t n = 1) -> ArrivalToken;
9      void Wait(ArrivalToken&& token) const;
10 };
```


Support library: ion

Benchmark

One measure of synchronization latency

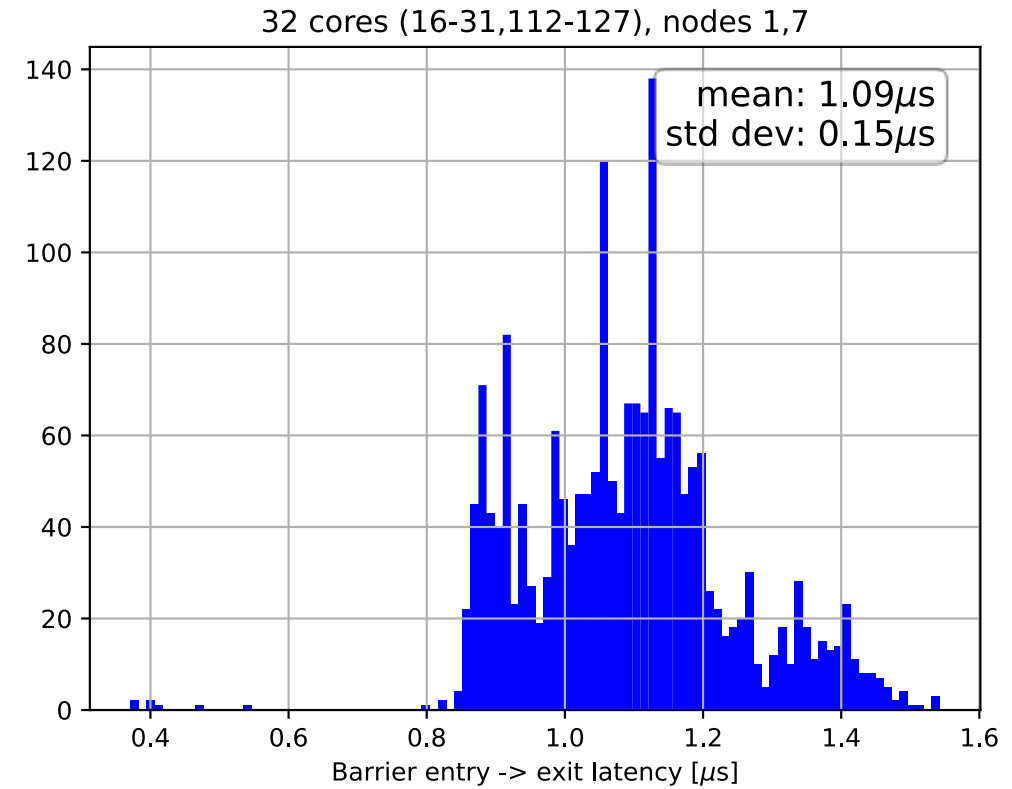
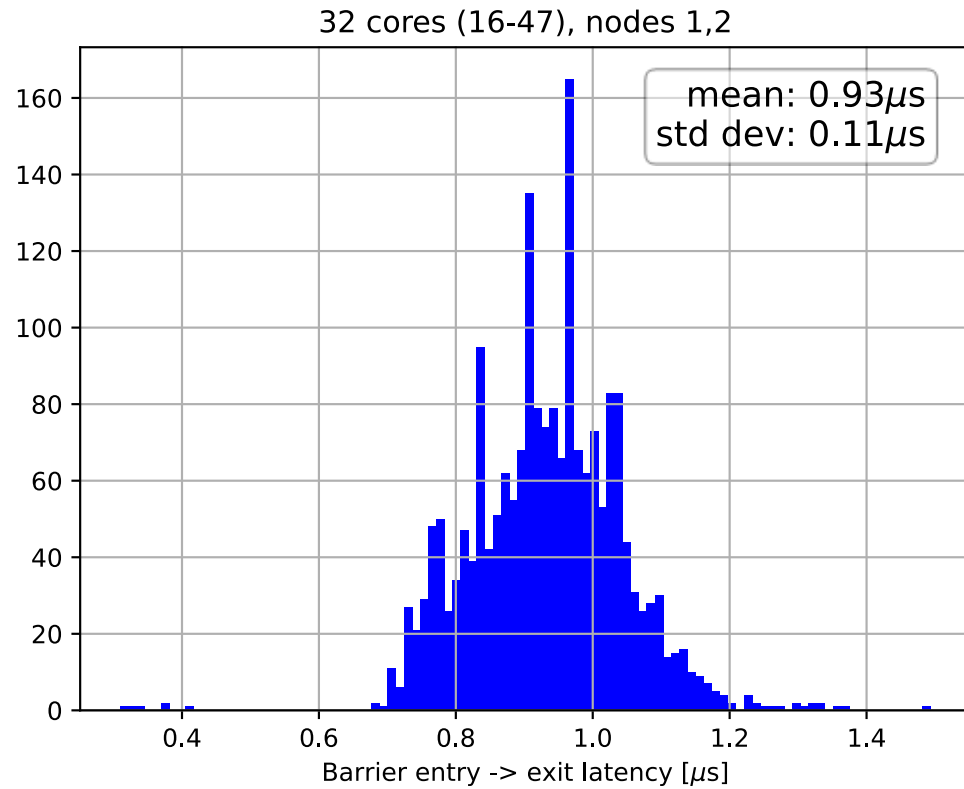
- Time between last thread arriving and first thread leaving barrier
- Empty completion function
- Last 2'000 samples from 10'000 iterations



AMD EPYC 7763 (128 Core Zen 3 Milan)

Support library: ion

As threads and NUMA distance increase so does latency



AMD EPYC 7763 (128 Core Zen 3 Milan)



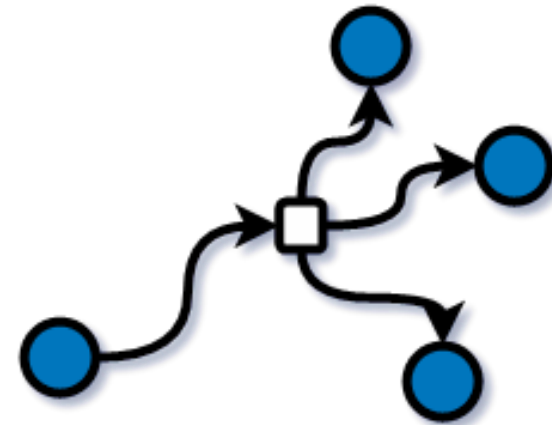
Signals

Signals

Notify, observe, wait

WFRTC use signals to

- Request state change (state enumeration)
- Indicate current state (state enumeration)
- Trigger telemetry sending (sample id)



Support library: ion

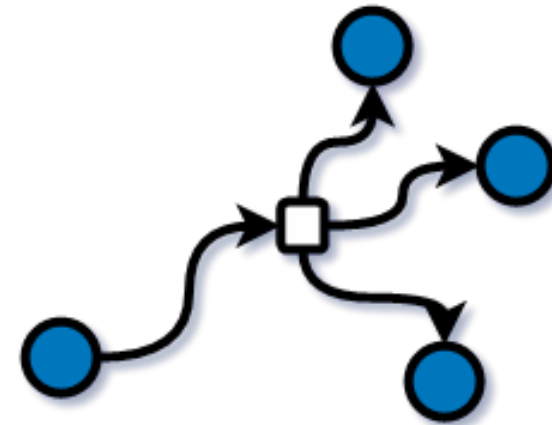
Thin abstraction on top of `std::atomic<T>`

`ion::SignalSource<T>`

- Lightweight thread safe, lock-free atomic value of type T
- No synchronization or order constraints

`ion::SignalToken<T>`

- Keeps association with source and remember last value





Support library: ion

```
1  template <class T>
2  class SignalSource {
3  public:
4      using ValueType = T;
5      explicit SignalSource(ValueType initial = ValueType{});
6      auto Load() const -> ValueType;
7      void Store(ValueType value);
8
9      auto CompareExchangeWeak(ValueType& expected, ValueType desired) -> bool;
10     auto CompareExchangeStrong(ValueType& expected, ValueType desired) -> bool;
11 };
12
13 template <class T, class UnaryOperation>
14 auto CompareExchangeTransform(SignalSource<T>& signal,
15                               UnaryOperation op) noexcept -> T;
```

Support library: ion

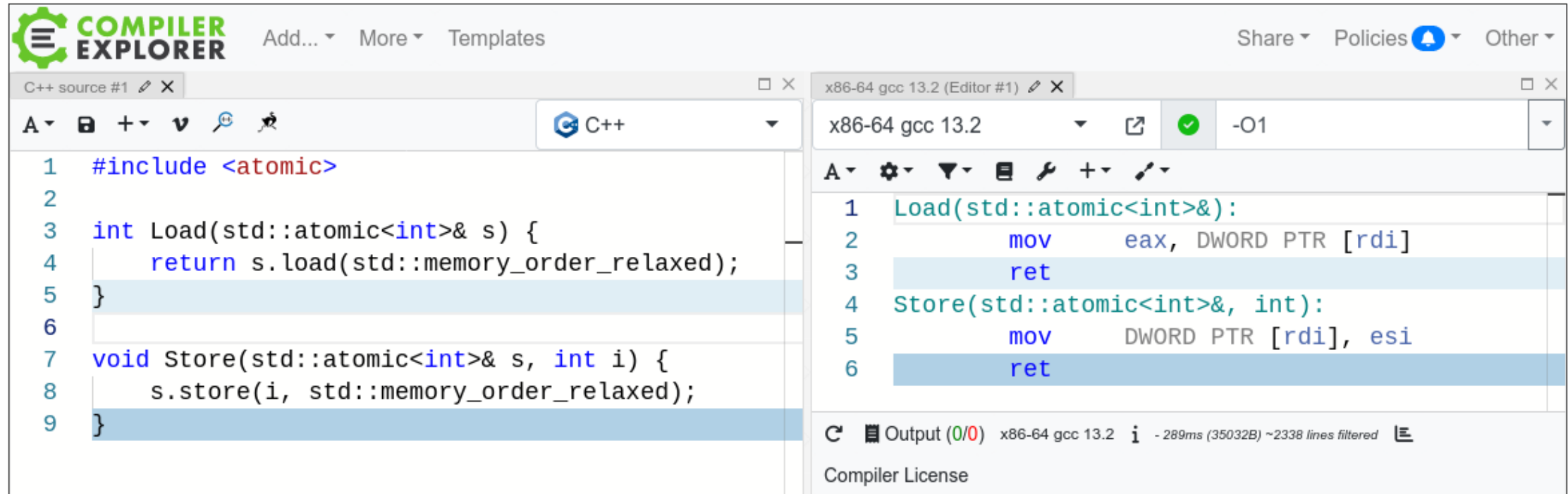
```

1  template <class T>
2  class SignalToken {
3  public:
4      using ValueType = T;
5      SignalToken(SignalSource<T> const* signal,
6                  std::optional<ValueType> last = std::nullopt);
7
8      auto Load() const -> ValueType;
9      auto GetLast() const -> ValueType;
10     void SetLast(ValueType last);
11     auto Update() -> ValueType;
12 };
13
14 template <class T>
15 auto Wait(SignalToken<T>& token) -> T;
16
17 template <class T, class Predicate>
18 auto Wait(SignalToken<T>& token, Predicate&& stop_waiting) -> T;
19
20 template <class T1, class T2>
21 auto WaitAny(SignalToken<T1>& token1, SignalToken<T2>& token2)
22     -> WaitAnyResult<T1, T2>;

```

Support library: ion

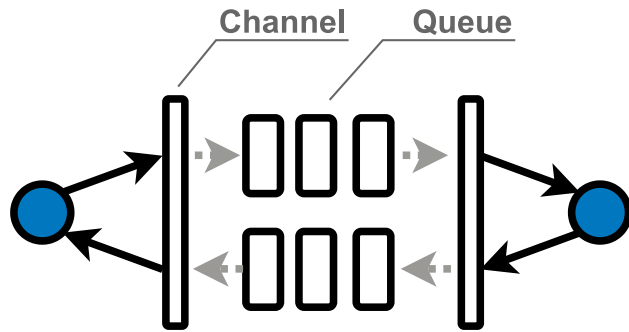
Signals under the hood



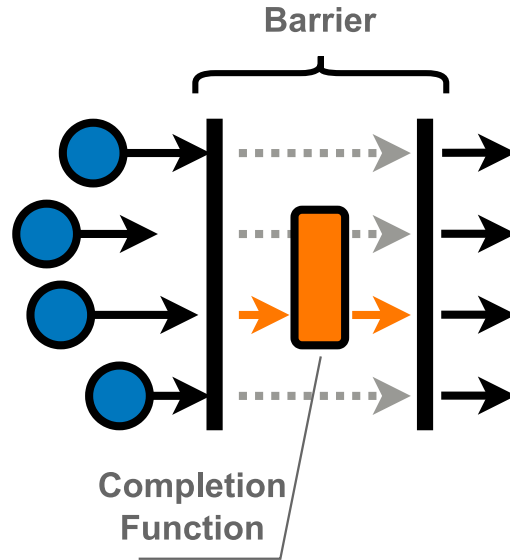
The screenshot shows the Compiler Explorer interface. On the left, the C++ source code is displayed in a window titled 'C++ source #1'. The code defines two functions: `Load` and `Store`, both using `std::atomic<int>` and `std::memory_order_relaxed`. On the right, the assembly output is shown in a window titled 'x86-64 gcc 13.2 (Editor #1)'. The assembly for `Load` consists of a `mov` instruction to load a `DWORD PTR` from `[rdi]` into `eax`, followed by a `ret` instruction. The assembly for `Store` consists of a `mov` instruction to store the `DWORD PTR` in `esi` to `[rdi]`, followed by a `ret` instruction. The bottom status bar indicates the compiler is 'x86-64 gcc 13.2' with optimization level '-O1', and the compilation took 289ms for 2338 lines of filtered output.

<https://godbolt.org/z/6d7jMfefW>

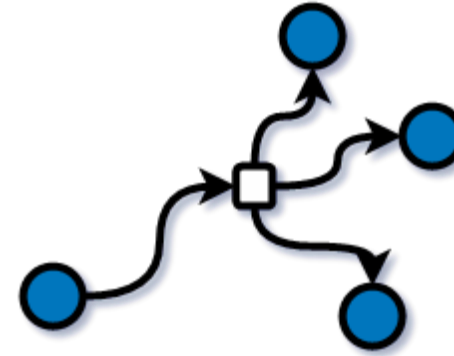
Summary



1:1



1:N:1



N:M






Thank you!

Calle Rosenquist
crosenqu@eso.org

Boost.Lockfree
www.boost.org/doc/libs/1_83_0/doc/html/lockfree.html

NUMA++ & ion
gitlab.eso.org/rtctk/roadrunner

-  @ESO Astronomy
-  @esoastronomy
-  @ESO
-  european-southern-observatory
-  @ESOobservatory





Bonus Slides

Barrier Example 1/2

```

1  #include <chrono>
2  #include <iostream>
3  #include <thread>
4  #include <vector>
5
6  #include <ion/barrier.hpp>
7
8  /** Completion function */
9  void Complete() noexcept {
10     std::cout << "Iteration beginning\n";
11 }
12
13 using Barrier = ion::Barrier<void (*)() noexcept>;
14
15 void Worker(Barrier& barrier, std::size_t num_iterations) {
16     for (auto count = 0u; count < num_iterations; ++count) {
17         // Synchronize start of each iteration
18         barrier.ArriveAndWait();
19
20         // Fake some work
21         using namespace std::chrono_literals;
22         std::this_thread::sleep_for(100ms);
23     }
24 }

```



Barrier Example 2/2

```
26 int main() {
27     auto const num_threads = 10;
28     auto const num_iterations = 100;
29
30     Barrier barrier(num_threads, &Complete);
31
32     std::vector<std::thread> threads;
33     threads.reserve(num_threads);
34     for (auto thread_idx = 0; thread_idx < num_threads; ++thread_idx) {
35         threads.emplace_back(&Worker, std::ref(barrier), num_iterations);
36     }
37
38     // Let it complete and then join
39     for (auto& thread : threads) {
40         thread.join();
41     }
42 }
```



Signal Example

```
1  #include <cstdlib>
2  #include <iostream>
3  #include <thread>
4
5  #include <ion/signal.hpp>
6
7  int main() {
8      auto source = ion::SignalSource<int>();
9
10     // To avoid race-condition between storing new signal value and what token
11     // initializes to we create the token in the main thread.
12     std::thread thread([token = ion::SignalToken<int>(&source)]() mutable {
13         auto current = Wait(token);
14         std::cout << "Got signal: " << current << std::endl;
15     });
16
17     // Update signal
18     source.Store(42);
19
20     thread.join();
21 }
```

Support library: ion

Benchmark

```
Running ionBenchmark
Run on (128 X 2450 MHz CPU s)
CPU Caches:
  L1 Data 32 KiB (x128)
  L1 Instruction 32 KiB (x128)
  L2 Unified 512 KiB (x128)
  L3 Unified 32768 KiB (x16)
Load Average: 0.00, 0.06, 0.12
```

Benchmark	Time	CPU	Iterations
BenchmarkSignalLoad/real_time/threads:1	0.410 ns	0.409 ns	1000000000
BenchmarkSignalLoad/real_time/threads:3	0.137 ns	0.409 ns	3000000000
BenchmarkSignalLoad/real_time/threads:5	0.082 ns	0.409 ns	5000000000
BenchmarkSignalLoad/real_time/threads:7	0.059 ns	0.409 ns	7000000000
BenchmarkSignalLoad/real_time/threads:9	0.046 ns	0.409 ns	9000000000
BenchmarkSignalLoad/real_time/threads:11	0.037 ns	0.409 ns	11000000000
BenchmarkSignalLoad/real_time/threads:13	0.032 ns	0.409 ns	13000000000
BenchmarkSignalLoad/real_time/threads:15	0.027 ns	0.409 ns	15000000000
BenchmarkSignalLoad/real_time/threads:16	0.026 ns	0.409 ns	16000000000
BenchmarkSignalStoreLoad/real_time/threads:1	0.410 ns	0.409 ns	1000000000
BenchmarkSignalStoreLoad/real_time/threads:3	0.204 ns	0.609 ns	3000000000
BenchmarkSignalStoreLoad/real_time/threads:5	0.125 ns	0.624 ns	5000000000
BenchmarkSignalStoreLoad/real_time/threads:7	0.094 ns	0.658 ns	6899152162
BenchmarkSignalStoreLoad/real_time/threads:9	0.075 ns	0.674 ns	9000000000
BenchmarkSignalStoreLoad/real_time/threads:11	0.063 ns	0.688 ns	10598253435
BenchmarkSignalStoreLoad/real_time/threads:13	0.056 ns	0.727 ns	12852271835
BenchmarkSignalStoreLoad/real_time/threads:15	0.047 ns	0.707 ns	14715672750
BenchmarkSignalStoreLoad/real_time/threads:16	0.044 ns	0.710 ns	15882213616

Divided by # threads

Under varying number of unpinned threads:

- SignalSource::Load()
- SignalSource::Store() [thread 1]
SignalSource::Load() [threads 2..N-1]

AMD EPYC 7763 (128 Core Zen 3 Milan)