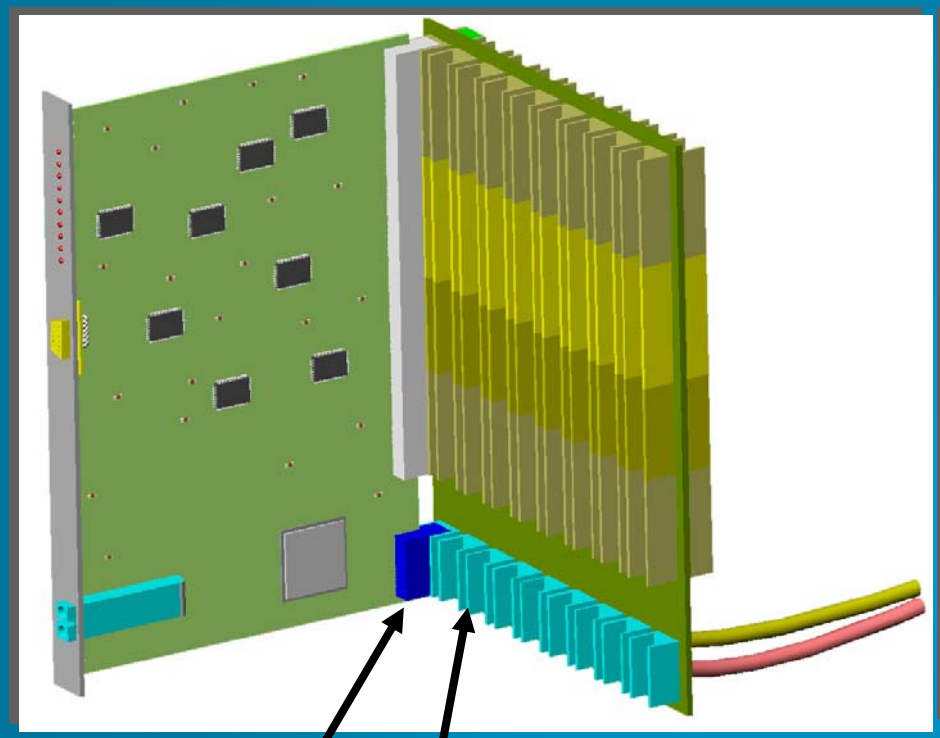
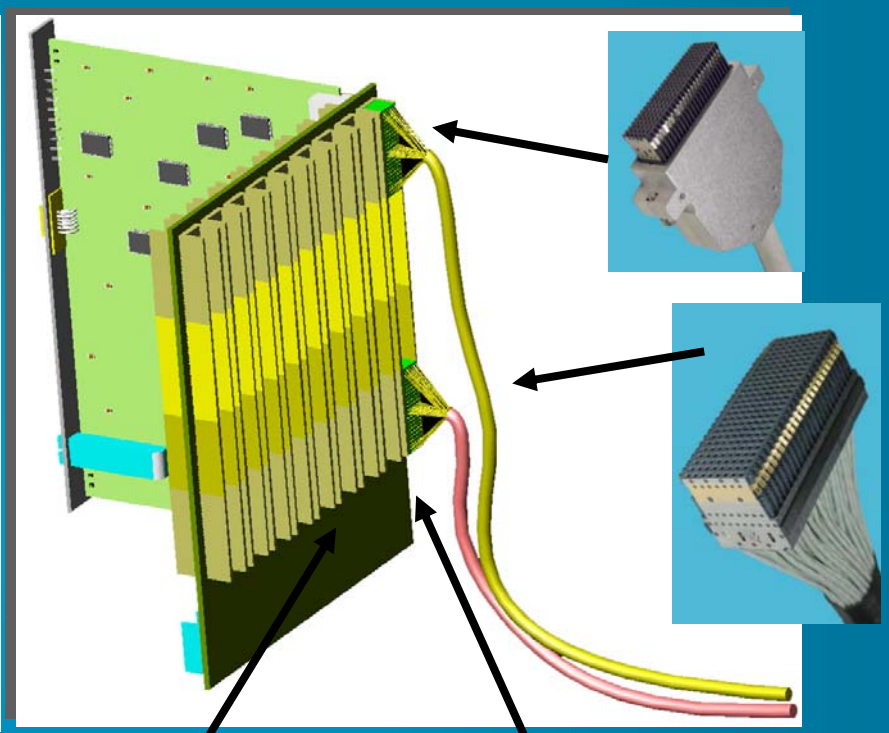
NGC progress, common platform and deliverables

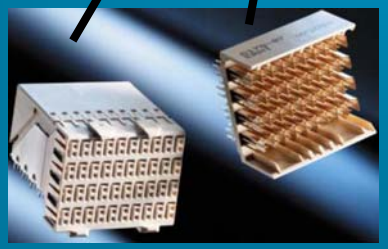
GENERAL

- All forthcoming detectors are easy to address individually
- The complexity is in reaching commonality due to the diversity in requirements
- Conceive a flexible and common platform
- Some technical open questions still to be solved
- We don’t have a united view yet
- We have not agreed yet on whether this is a one-person project or several-person project

NGC common platform for standard systems

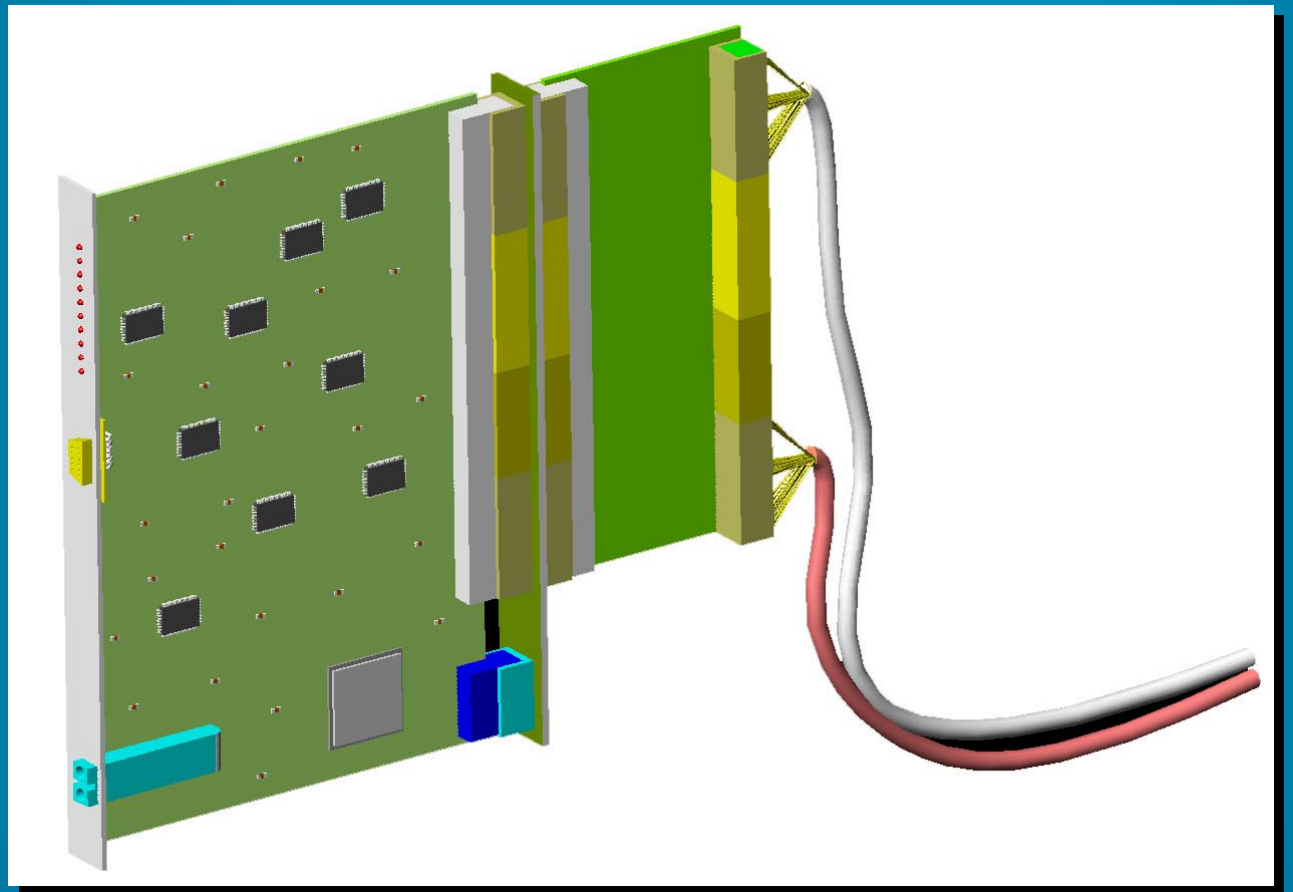






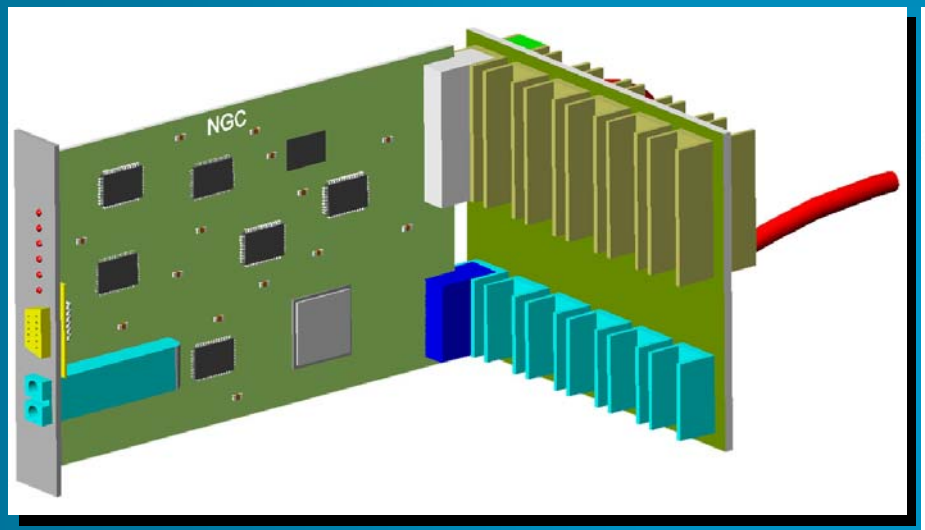
NGC common hardware platform

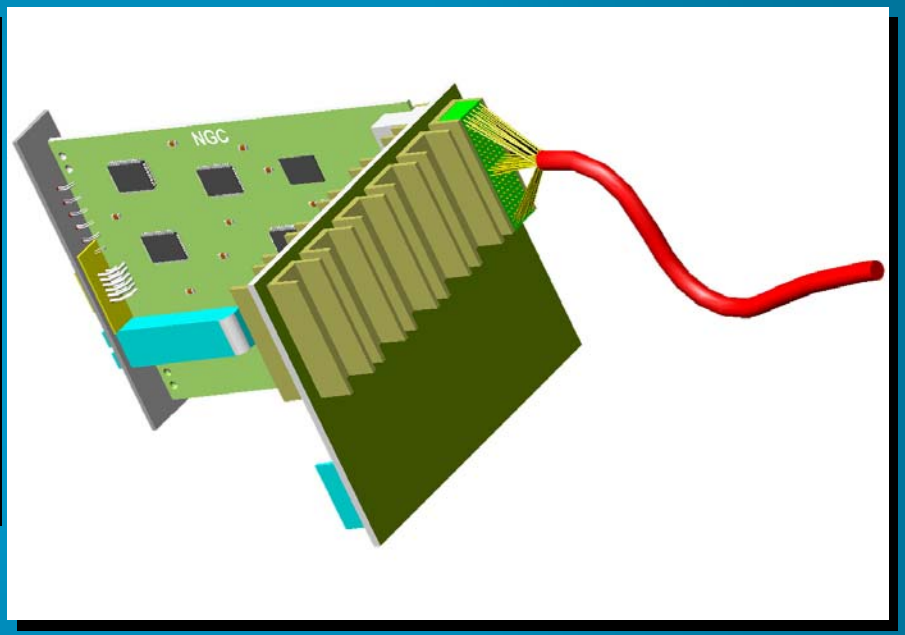
- Standard line card set
- Specially cases covered with add-ons



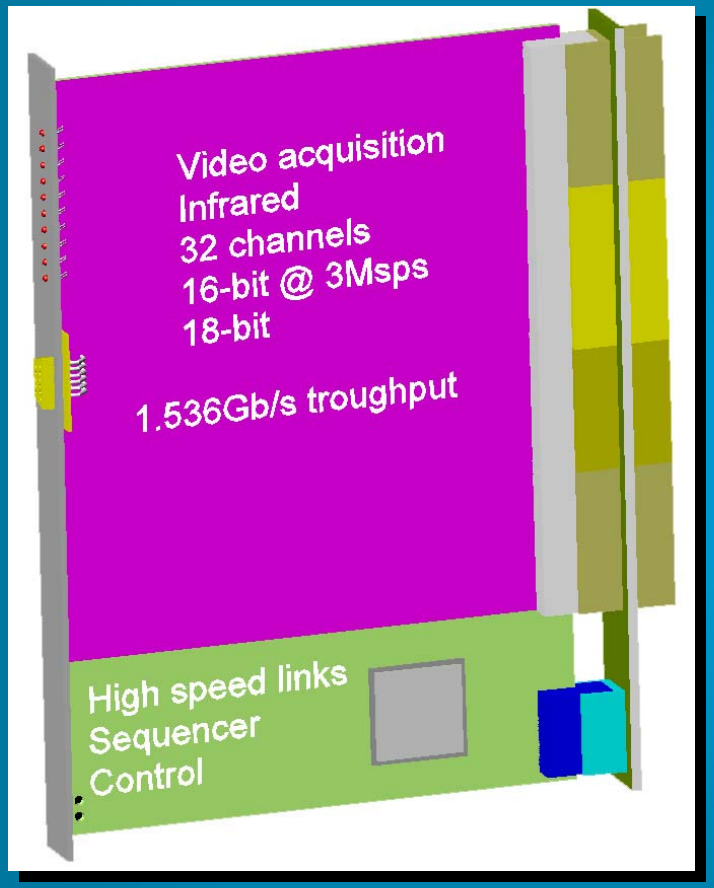
NGC common hardware platform
3U option

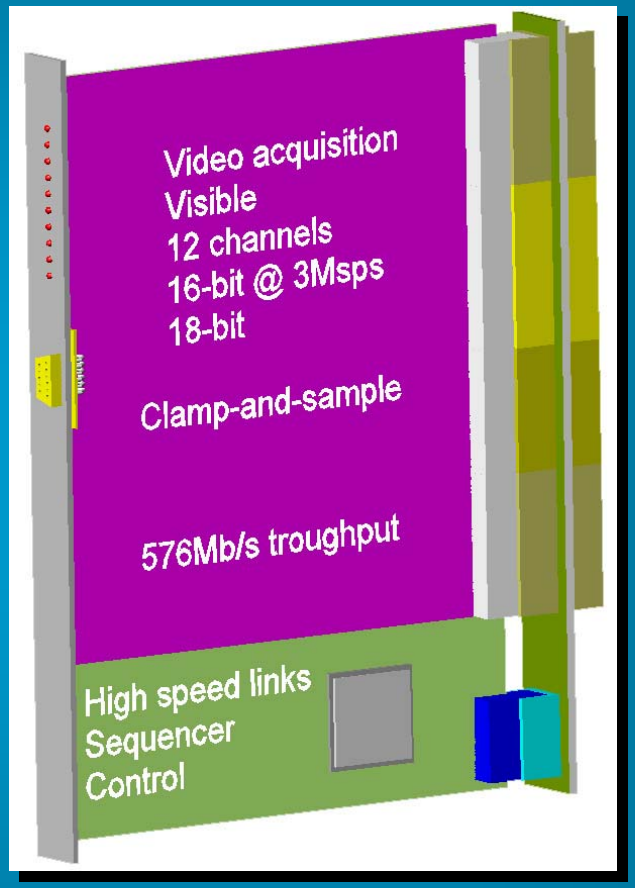
The common platform allows 3U electronics





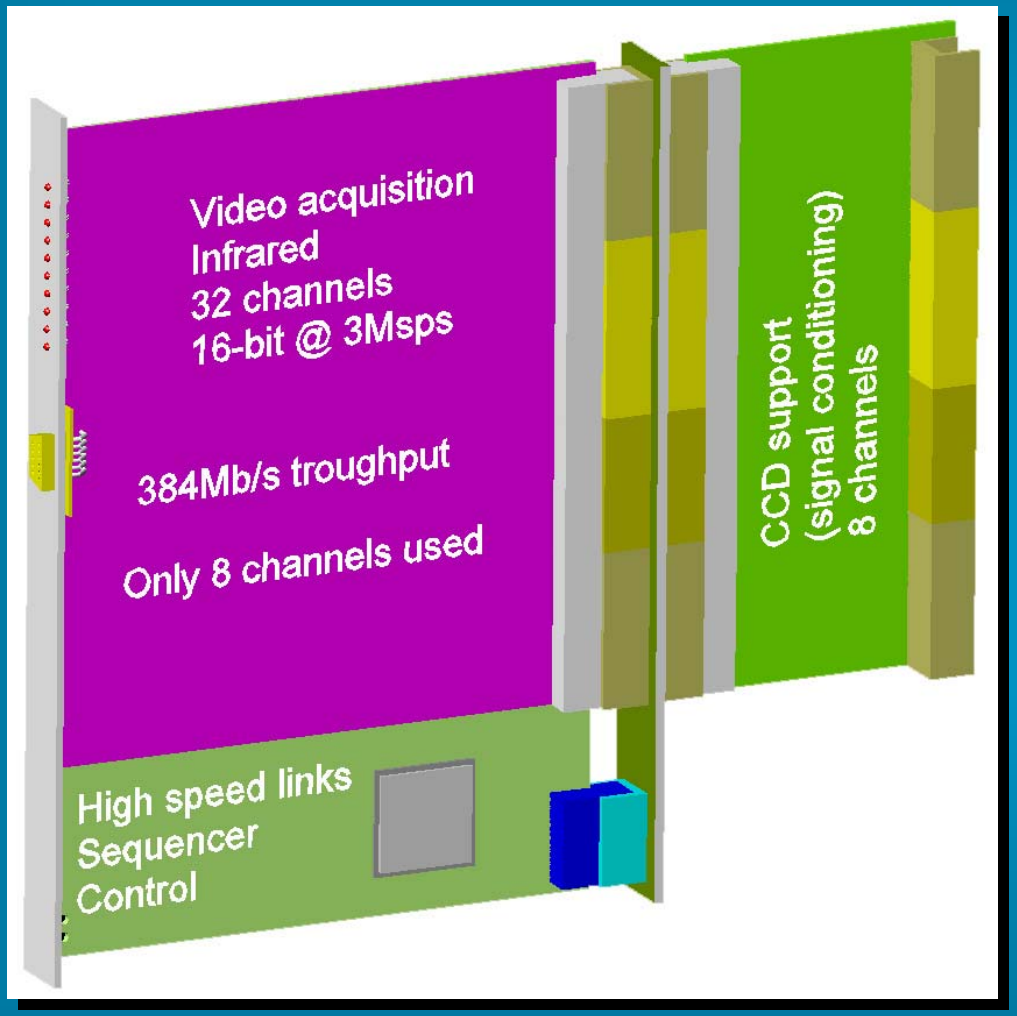
Deliverables
Video acquisition board



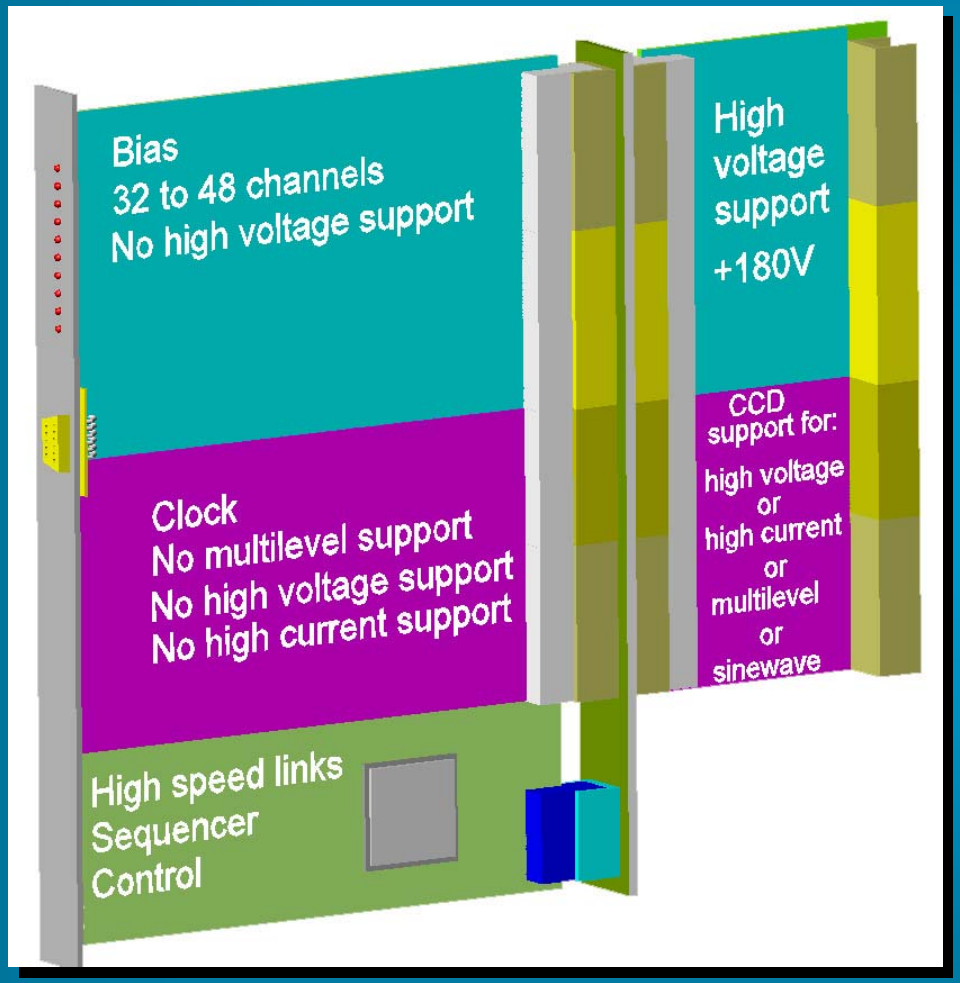


Design subjected to trustful oversampling test

Deliverables
Video acquisition board alternative



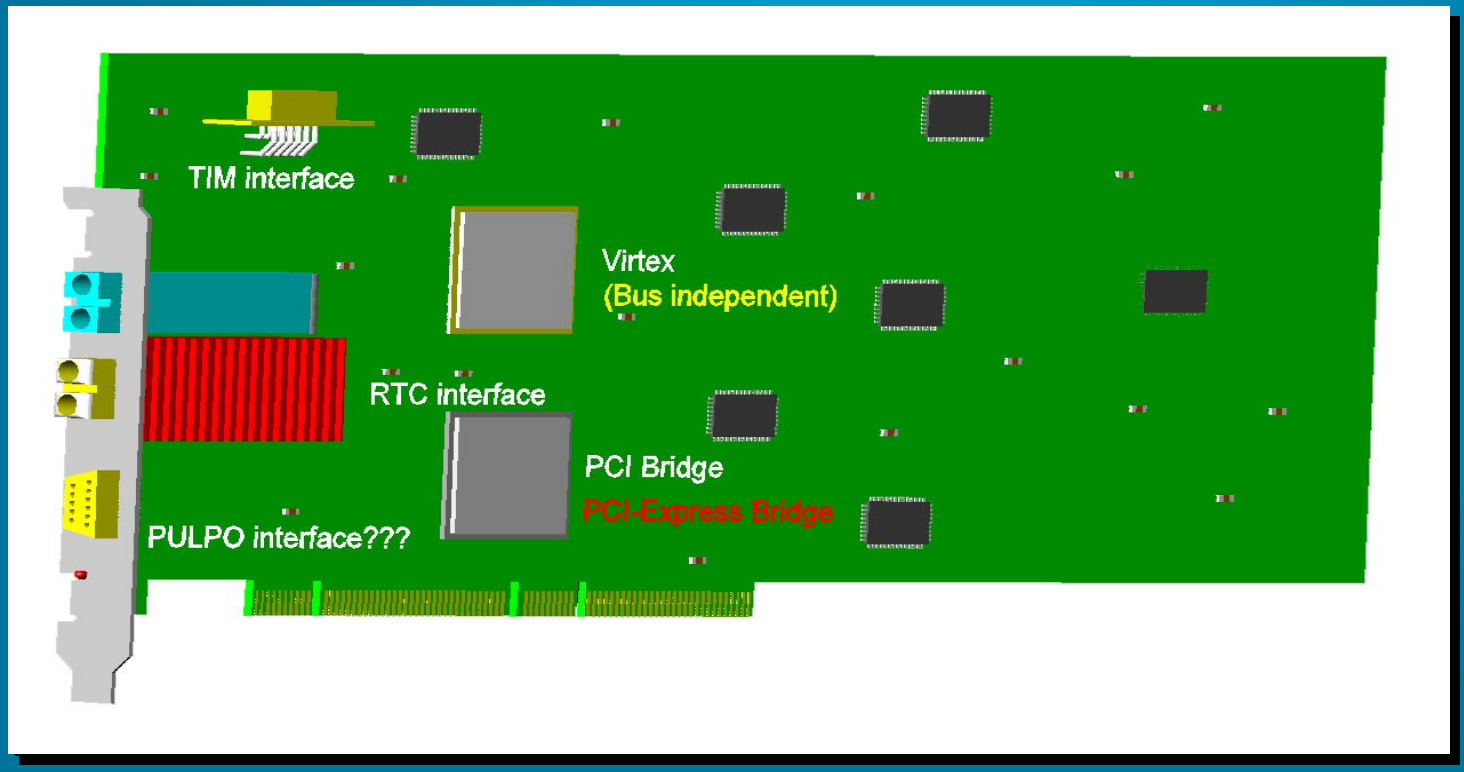
Deliverables
Clock/Bias board



The platform supports add-ons for special detectors and does not limit future design of dedicated boards

Deliverables
Back-end board

- RTC interface in agreement with AO group
- TIM interface for absolute time triggering
- PULPO interface ???
- PCI-Express
- Decoupling detector specific function from computer bus: Two-chip solution



Work distribution

A possible work distribution for the hardware

	Some one else?	Mark	Christoph	Leander	Manfred	Roland	Javier	Jesper
Bias part of Clock/Bias board								
Clock part of the Clock/Bias board								
Acquisition board for the infrared								
Acquisition board for the visible (*)								
Communication board								
Backplane and backplane communication								
Multilevel support on clock board (**)								
PCI board on back-end								
High voltage bias support								
Testing support board								
Housing								
Cabling								
Cooling (heat sinks or active)								